

Silicon PNP Power Transistors

2SB1133

DESCRIPTION

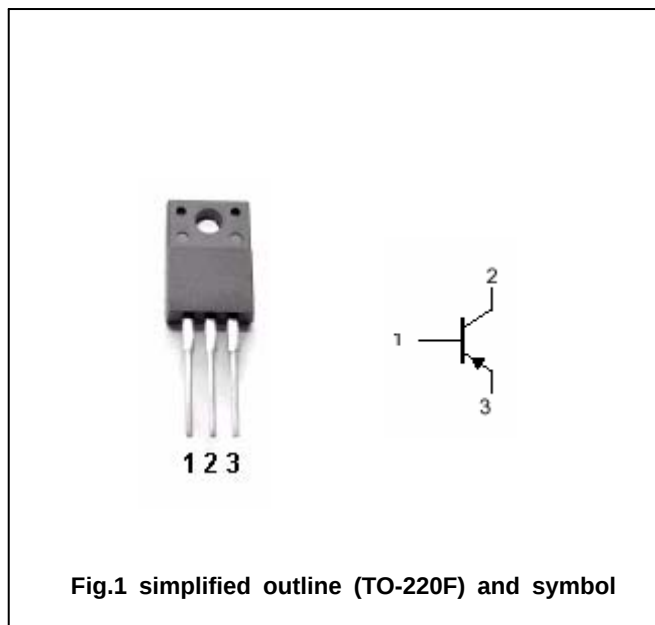
- With TO-220F package
- Complement to type 2SD1666
- Low collector saturation voltage
- Wide area of safe operation

APPLICATIONS

- For low-frequency and general-purpose amplifier applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

Absolute maximum ratings ($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	CONDITIONS	MAX	UNIT
V_{CBO}	Collector-base voltage	Open emitter	-60	V
V_{CEO}	Collector-emitter voltage	Open base	-60	V
V_{EBO}	Emitter-base voltage	Open collector	-6	V
I_C	Collector current		-3	A
I_{CM}	Collector current-peak		-8	A
P_C	Collector dissipation	$T_a=25$	2	W
		$T_C=25$	25	
T_j	Junction temperature		150	
T_{stg}	Storage temperature		-40~150	

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CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-5mA ; R _{BE} =∞	-60			V
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =-1mA ; I _E =0	-60			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =-1mA ; I _C =0	-6			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-2A ; I _B =-0.2A			-1.0	V
V _{BE}	Base-emitter saturation voltage	I _C =-0.5A ; V _{CE} =-5V			-1.0	V
I _{CBO}	Collector cut-off current	V _{CB} =-40V ; I _E =0			-100	μA
I _{EBO}	Emitter cut-off current	V _{EB} =-4V ; I _C =0			-100	μA
h _{FE-1}	DC current gain	I _C =-0.5A ; V _{CE} =-5V	70		280	
h _{FE-2}	DC current gain	I _C =-3A ; V _{CE} =-5V	20			
C _{OB}	Output capacitance	I _E =0 ; V _{CB} =-10V ; f=1MHz		110		pF
f _T	Transition frequency	I _C =-0.5A ; V _{CE} =-5V		40		MHz

◆ h_{FE-1} Classifications

Q	R	S
70-140	100-200	140-280

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PACKAGE OUTLINE

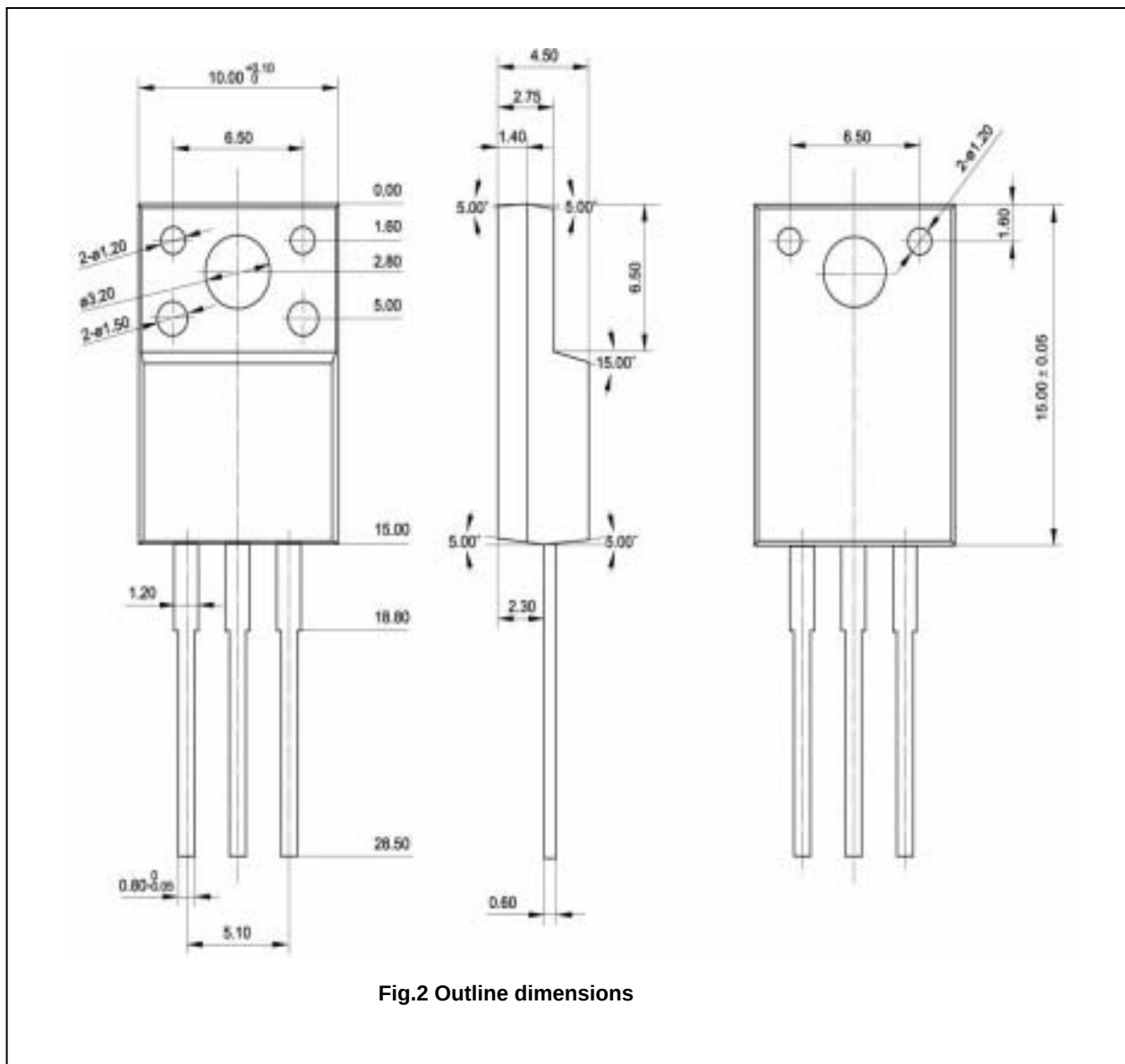


Fig.2 Outline dimensions

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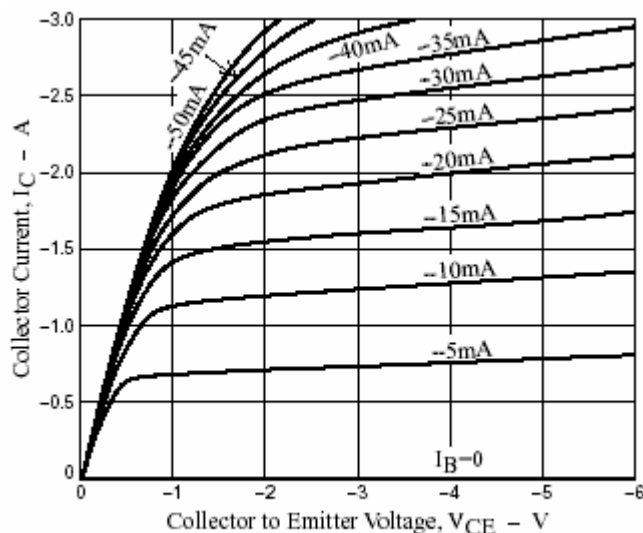


Fig.3 Static Characteristic

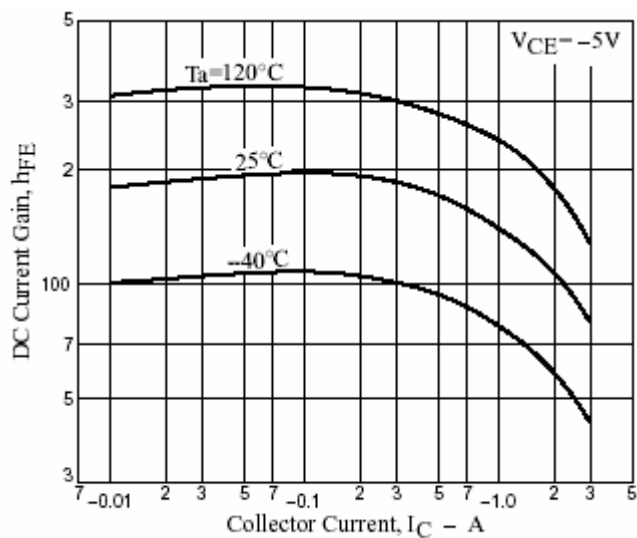


Fig.4 DC current Gain

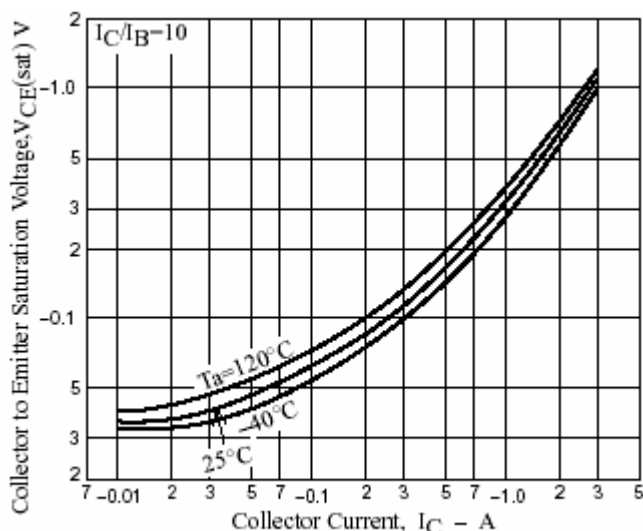


Fig.5 Collector-Emitter Saturation Voltage

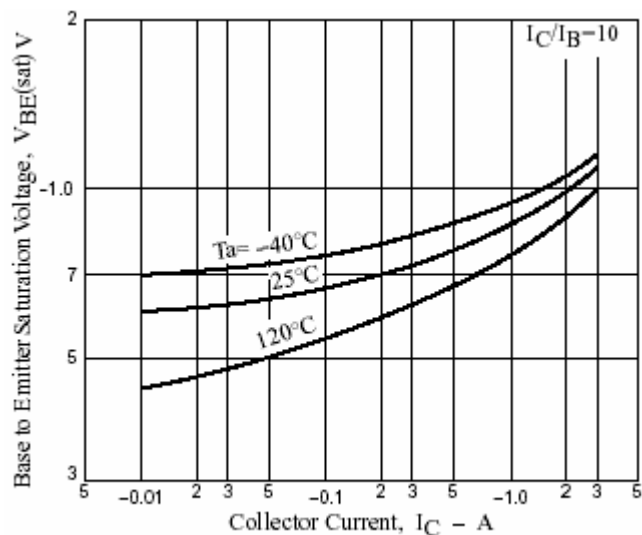


Fig.6 Base-Emitter Saturation Voltage

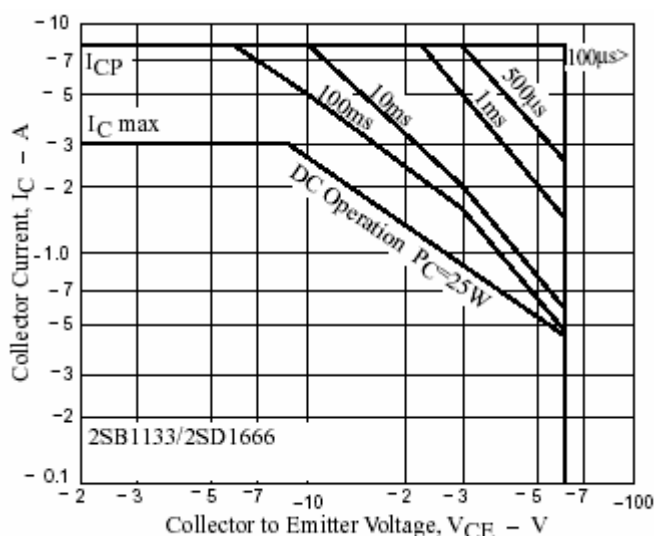


Fig.7 Safe Operating Area