

Silicon NPN Power Transistors

2SD1553

DESCRIPTION

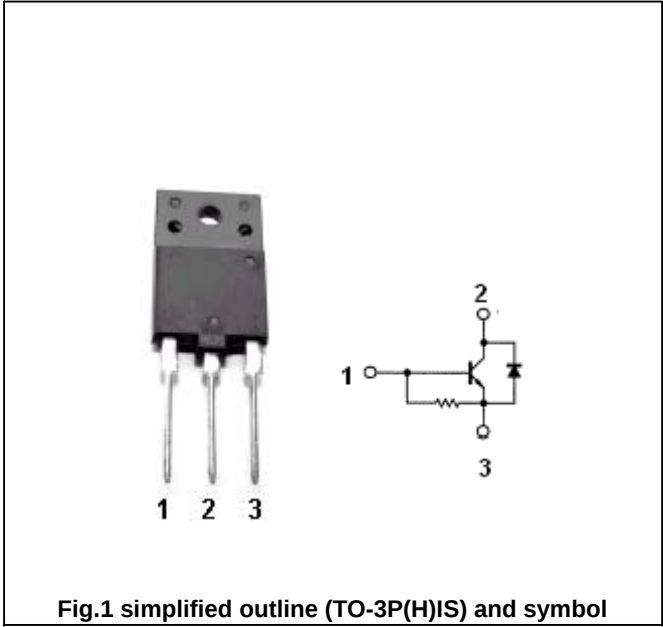
- With TO-3P(H)IS package
- Built-in damper diode
- High voltage ,high speed
- Low collector saturation voltage

APPLICATIONS

- For color TV horizontal output applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter



ABSOLUTE MAXIMUM RATINGS AT Tc=25

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	1500	V
V _{CEO}	Collector-emitter voltage	Open base	600	
V _{EBO}	Emitter-base voltage	Open collector	5	V
I _C	Collector current		2.5	A
I _B	Base current		1	A
P _C	Collector power dissipation		40	W
T _j	Junction temperature		150	
T _{stg}	Storage temperature		-55~150	

Silicon NPN Power Transistors**2SD1553****CHARACTERISTICS**T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{EB0}	Emitter-base breakdown voltage	I _E =200mA , I _C =0	5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =2A; I _B =0.6A		5.0	8.0	V
V _{BEsat}	Emitter-base saturation voltage	I _C =2A; I _B =0.6A			1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =500V; I _E =0			10	μ A
h _{FE}	DC current gain	I _C =0.5A ; V _{CE} =5V	8			
f _T	Transition frequency	I _C =0.1A ; V _{CE} =10V		3		MHz
C _{OB}	Collector output capacitance	I _E =0 ; V _{CB} =10V;f=1MHz		95		pF
V _F	Diode forward voltage	I _F =2.5A		1.6	2.0	V
t _f	Fall time	I _{CP} =2A ; I _{B1(end)} =0.6A		0.5	1.0	μ s

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PACKAGE OUTLINE

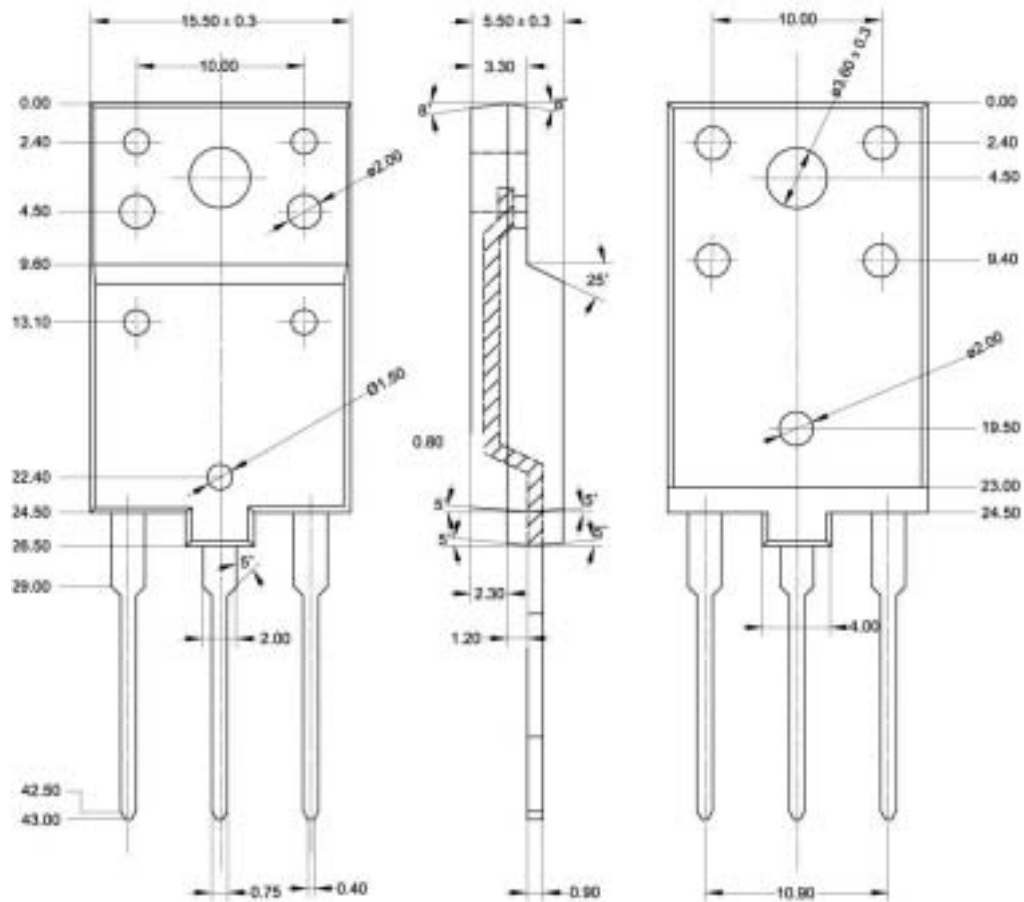


Fig.2 Outline dimensions (unindicated tolerance: ± 0.15 mm)